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APPLICANTS

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** CONTINUING DATA ***** *None*

** FOREIGN APPLICATIONS *****
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IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i>	INITIALS <i>[Initials]</i>		
Verified and Acknowledged				

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TITLE
 Wiring substrate, semiconductor device, semiconductor module, electronic equipment, method for designing wiring substrate, method for manufacturing semiconductor device, and method for manufacturing semiconductor module

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